

#3

part 4 Fig1-Fig24B

FIG. 1

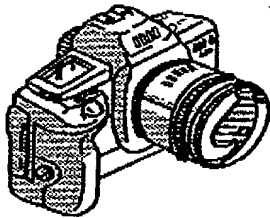
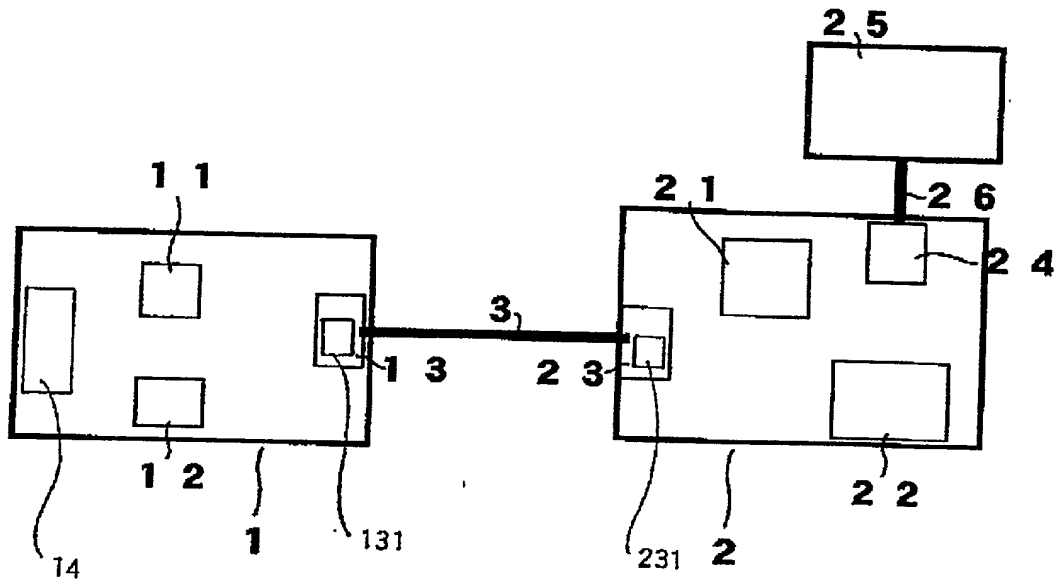


FIG. 1

FIG 3

130

131 (NUMERICAL VALUE PORTION) 132 (NAME PORTION)

1	0	0	N	I	K	0	N
1	0	1	A	B	C		
1	0	2	J	A	P	A	N
1	0	3	A	B	C		
1	0	4	N	I	K	0	N
.
.
.
.
9	9	9	*	*	*	*	*

FIG. 4

140 (FILE NAME)

141 (HEADER PORTION) 142 (NUMERICAL VALUE PORTION) 143 (EXTENSION)

D	S	C	N	0	0	0	1	.	J	P	G
D	S	C	N	0	0	0	2	.	J	P	G
D	S	C	N	0	0	0	3	.	J	P	G
.
.
.
D	S	C	N	9	9	9	9	.	J	P	G
I	N	F	O	.	T	X	T				

FIG. 5A

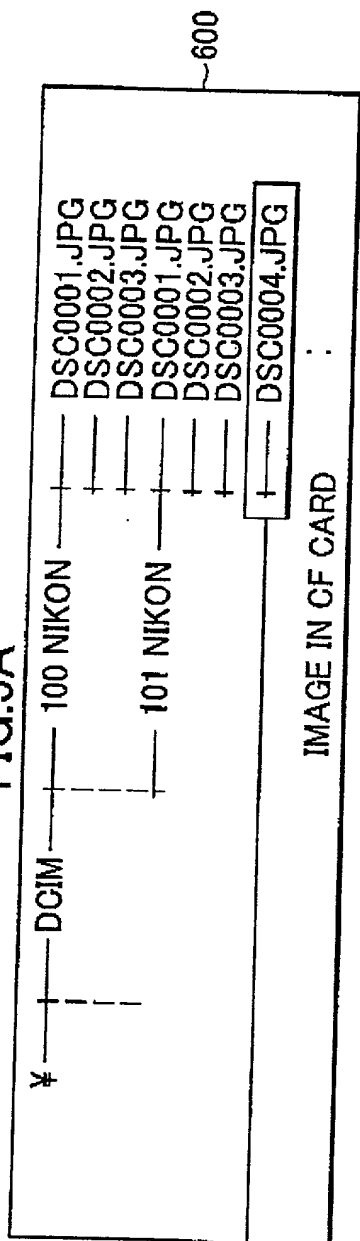


FIG. 5B

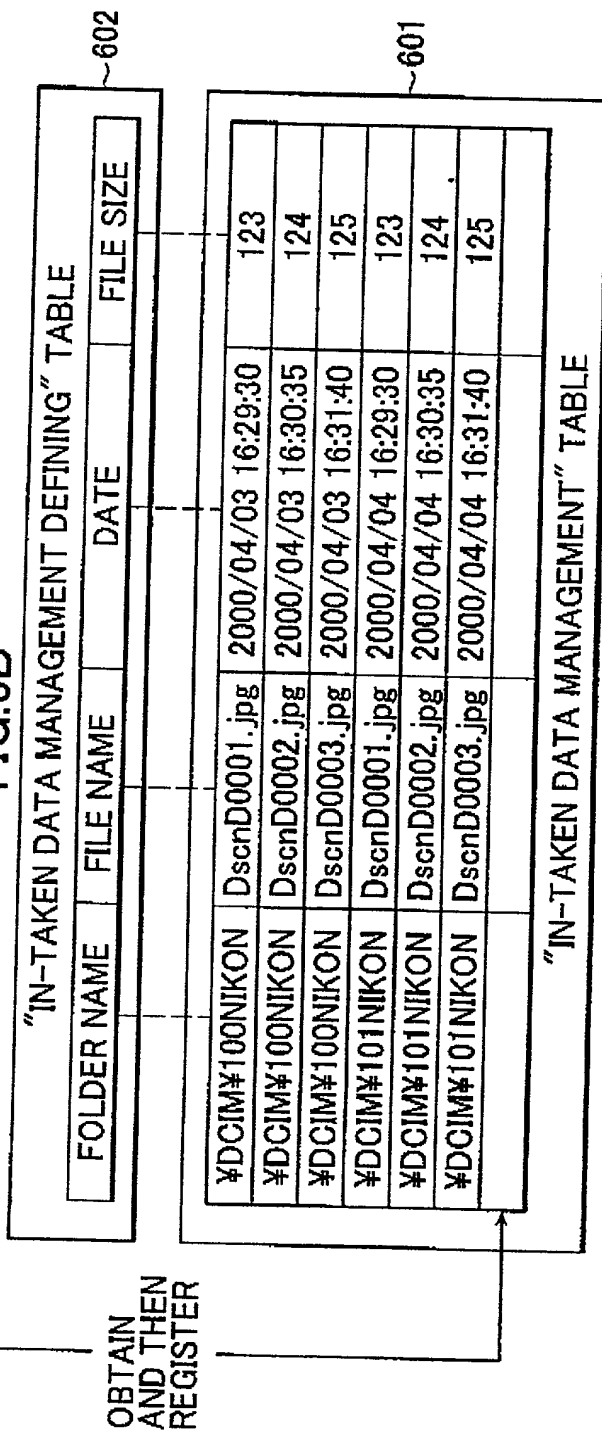


FIG.6

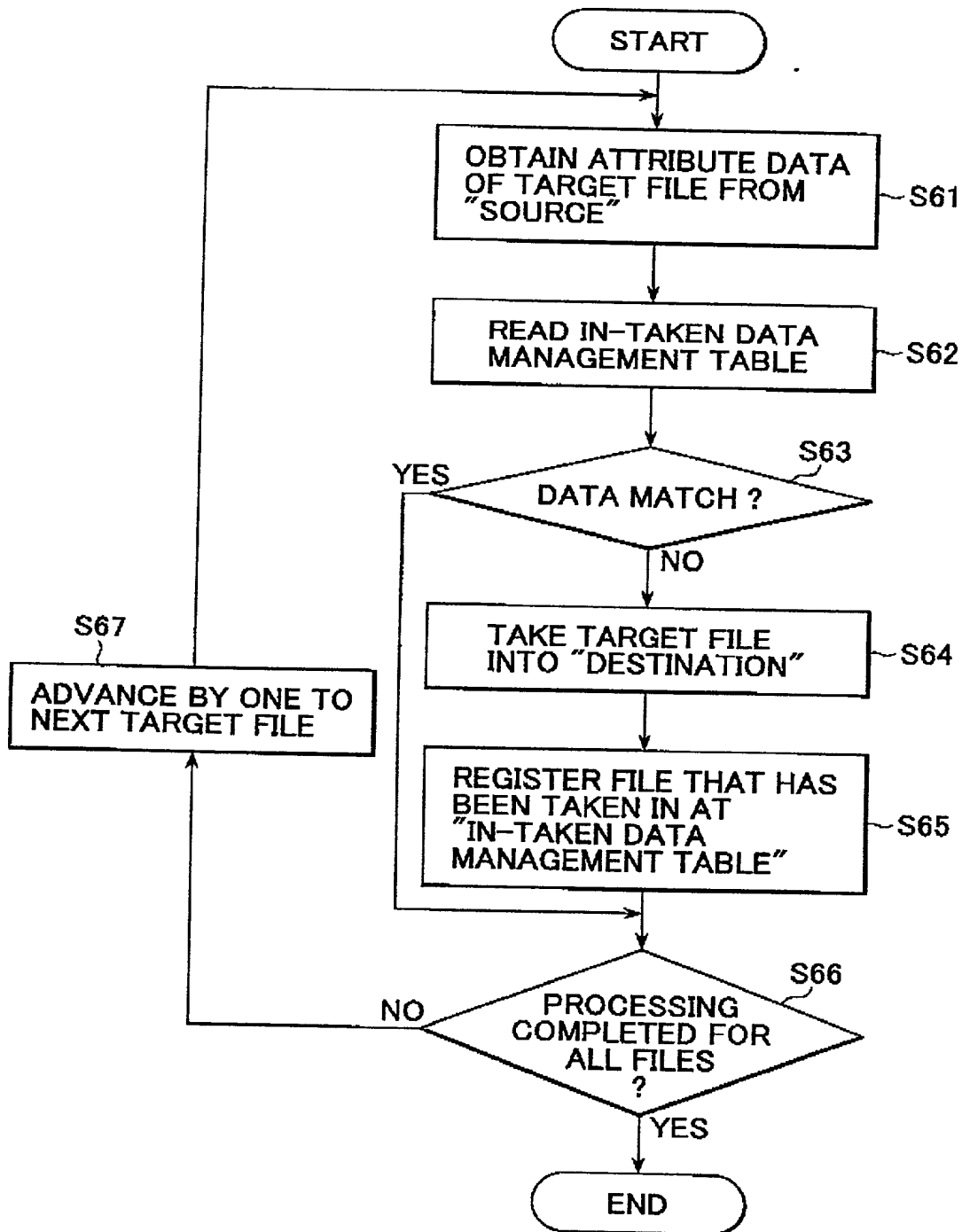


FIG. 7

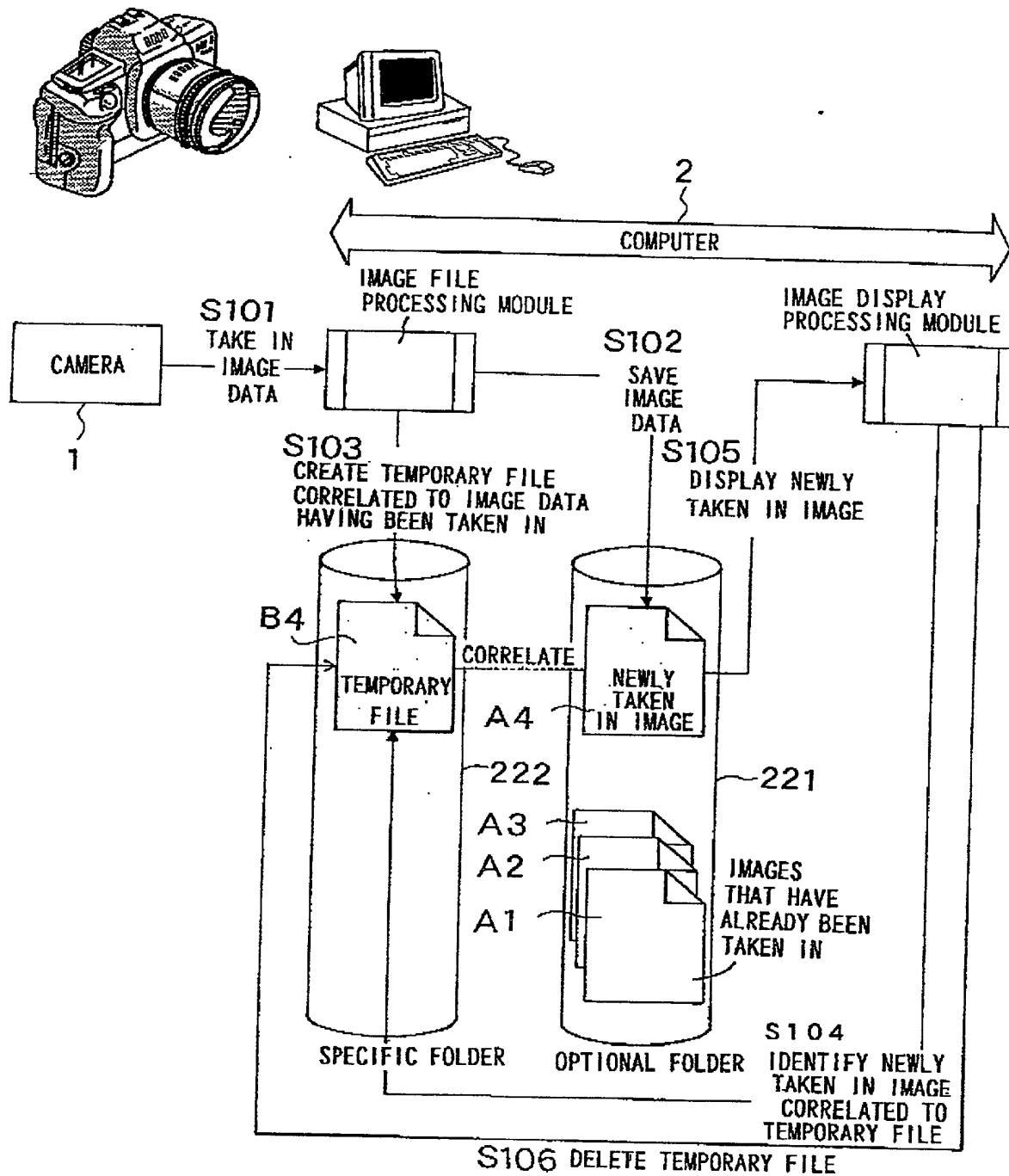


FIG.8

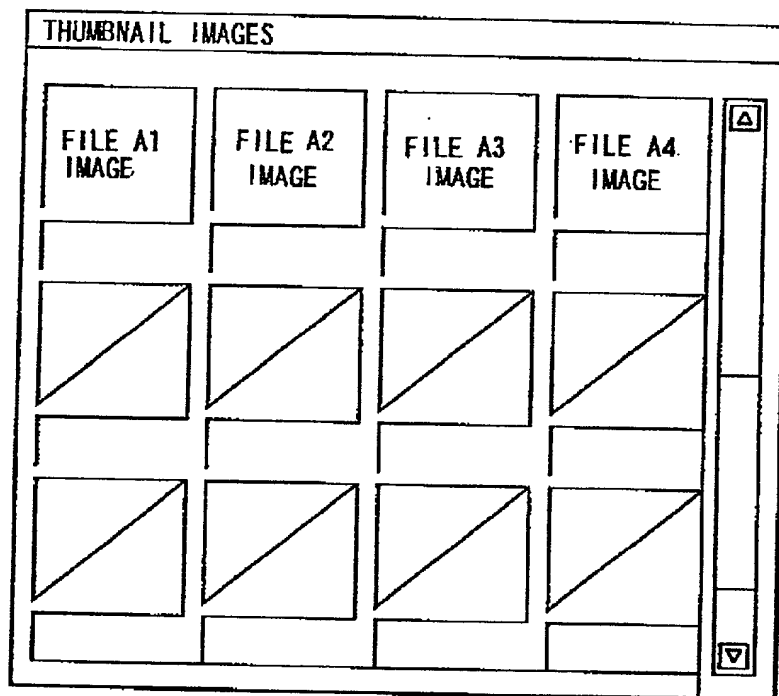


FIG. 9

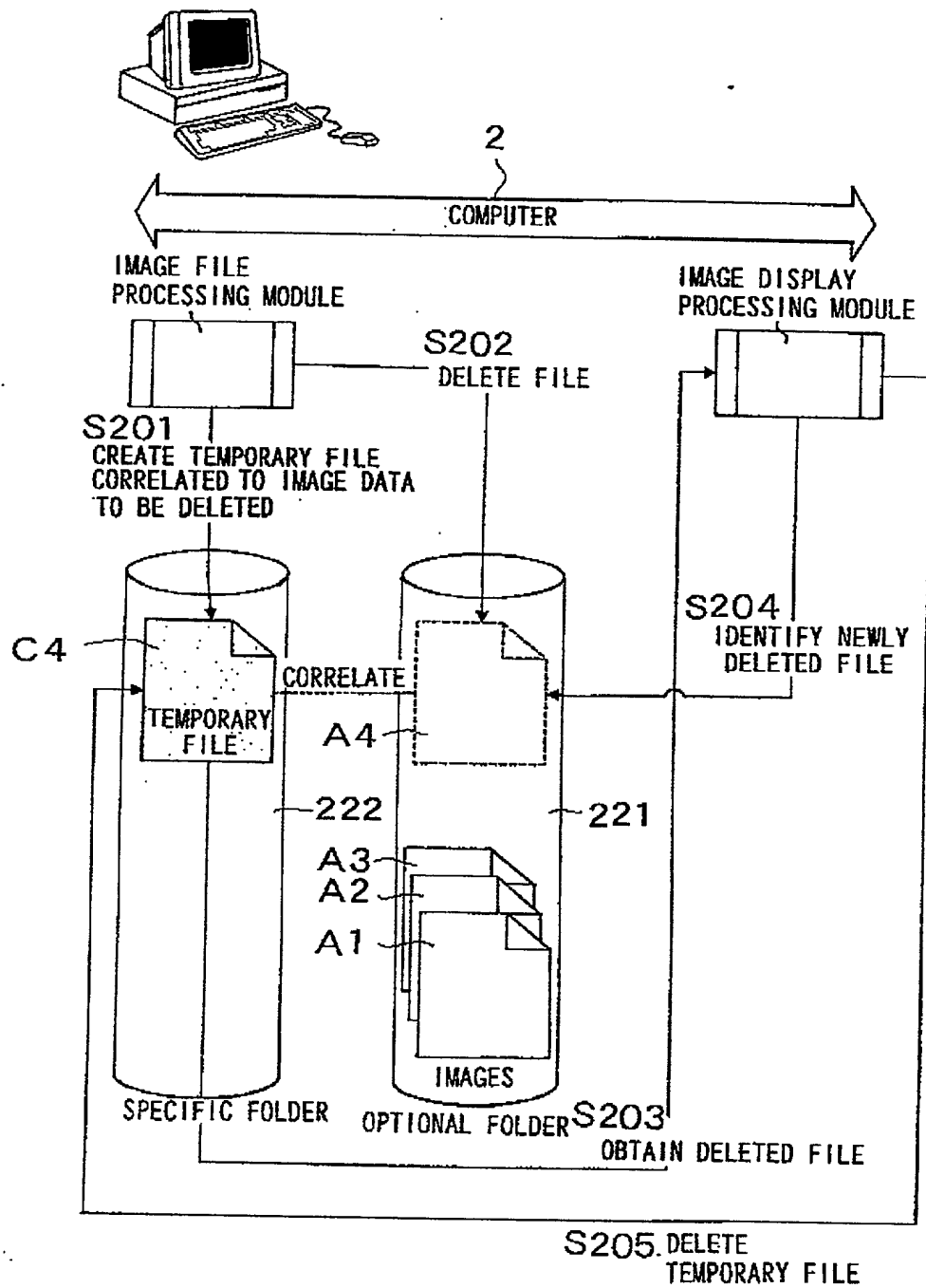


FIG.10

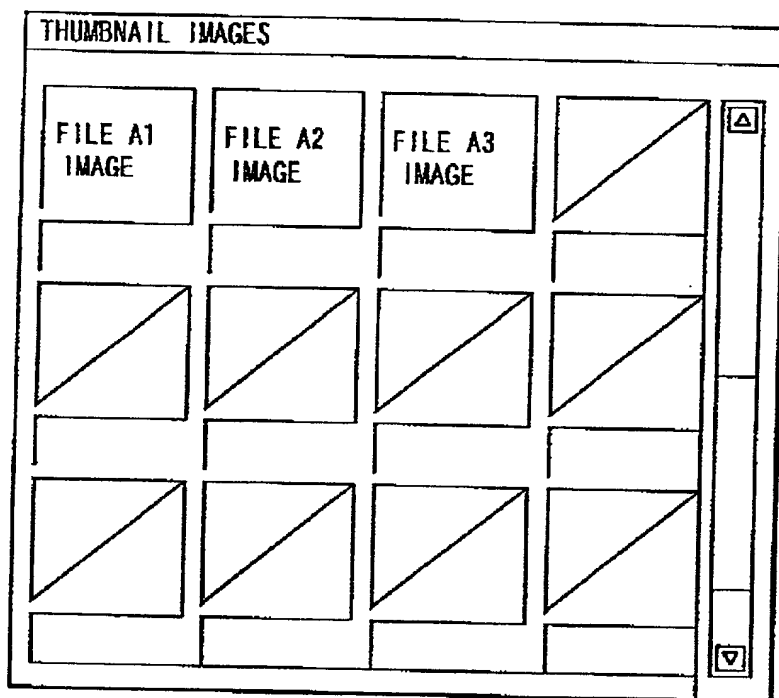


FIG.11

140			
141	142	150	160
DSCN0004. JPG	2000/02/25	14:05:00	
DSCN0005. JPG	2000/02/25	14:05:00	
DSCN0006. JPG	2000/02/25	14:05:01	
DSCN0007. JPG	2000/02/25	14:05:01	

FIG.12

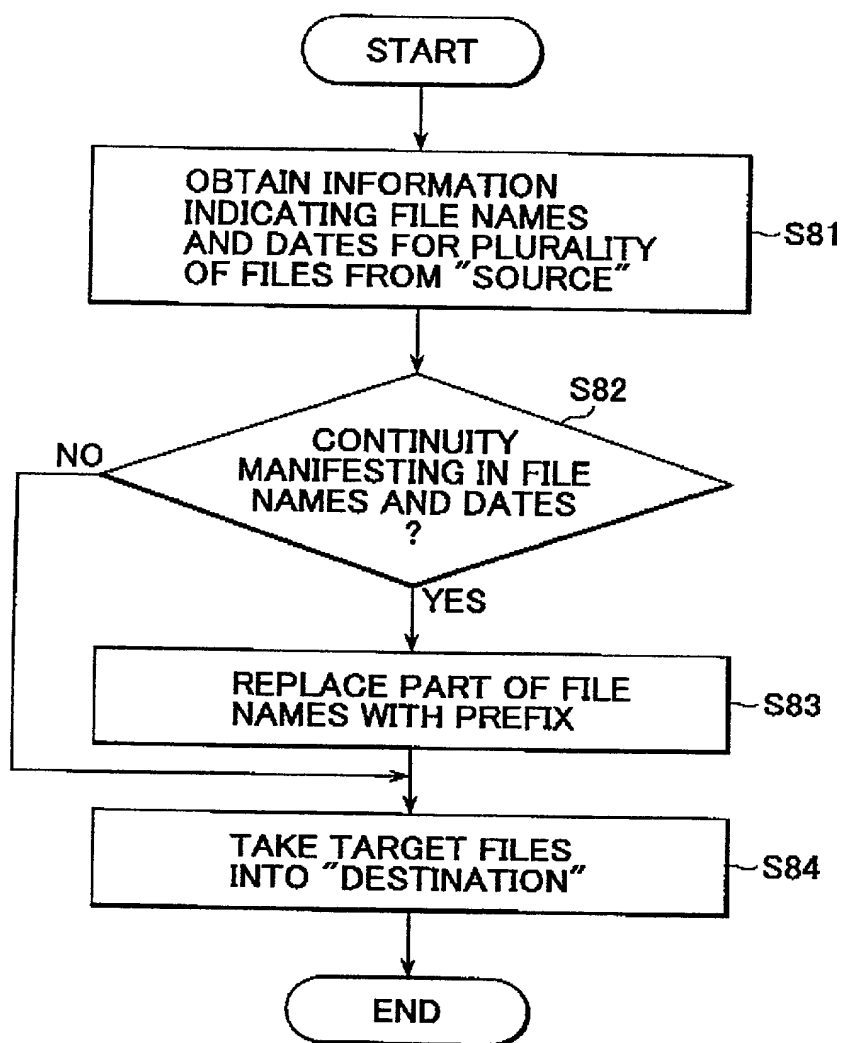


FIG.13

603

READ ONLY
2000/04/04 16:30:**

2000/04/04 16:30:**

FIG.14

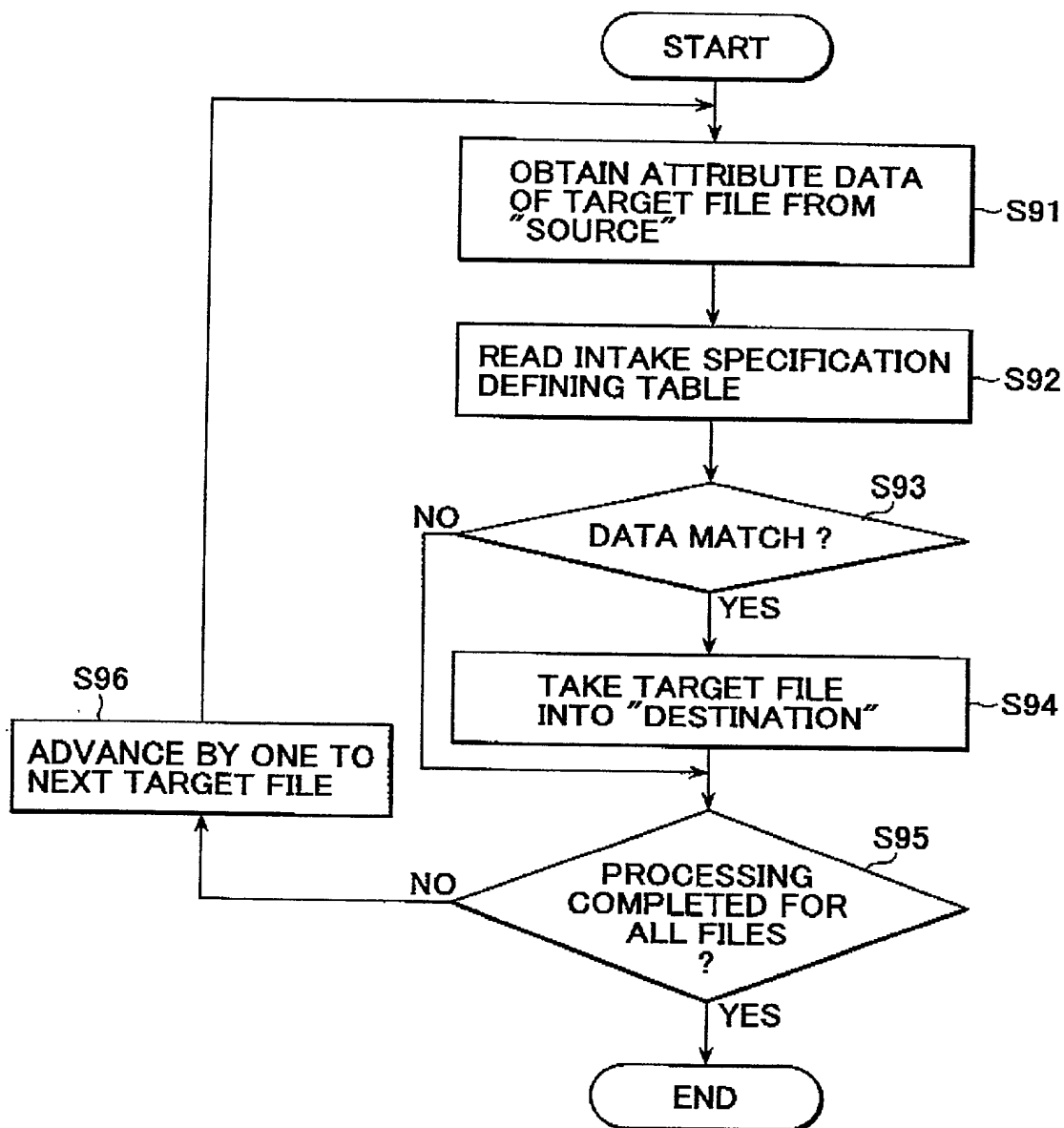


FIG.15

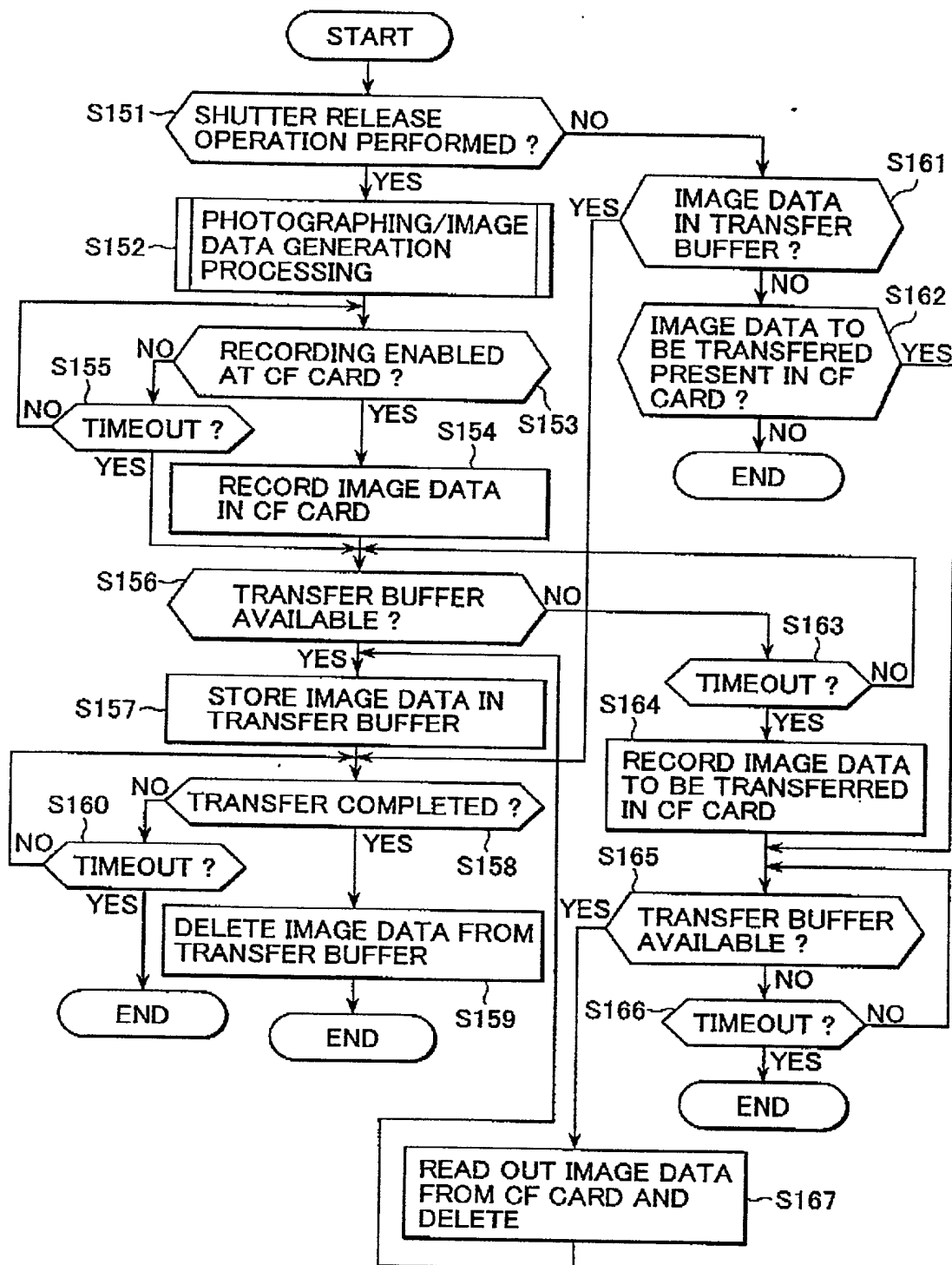


FIG.16

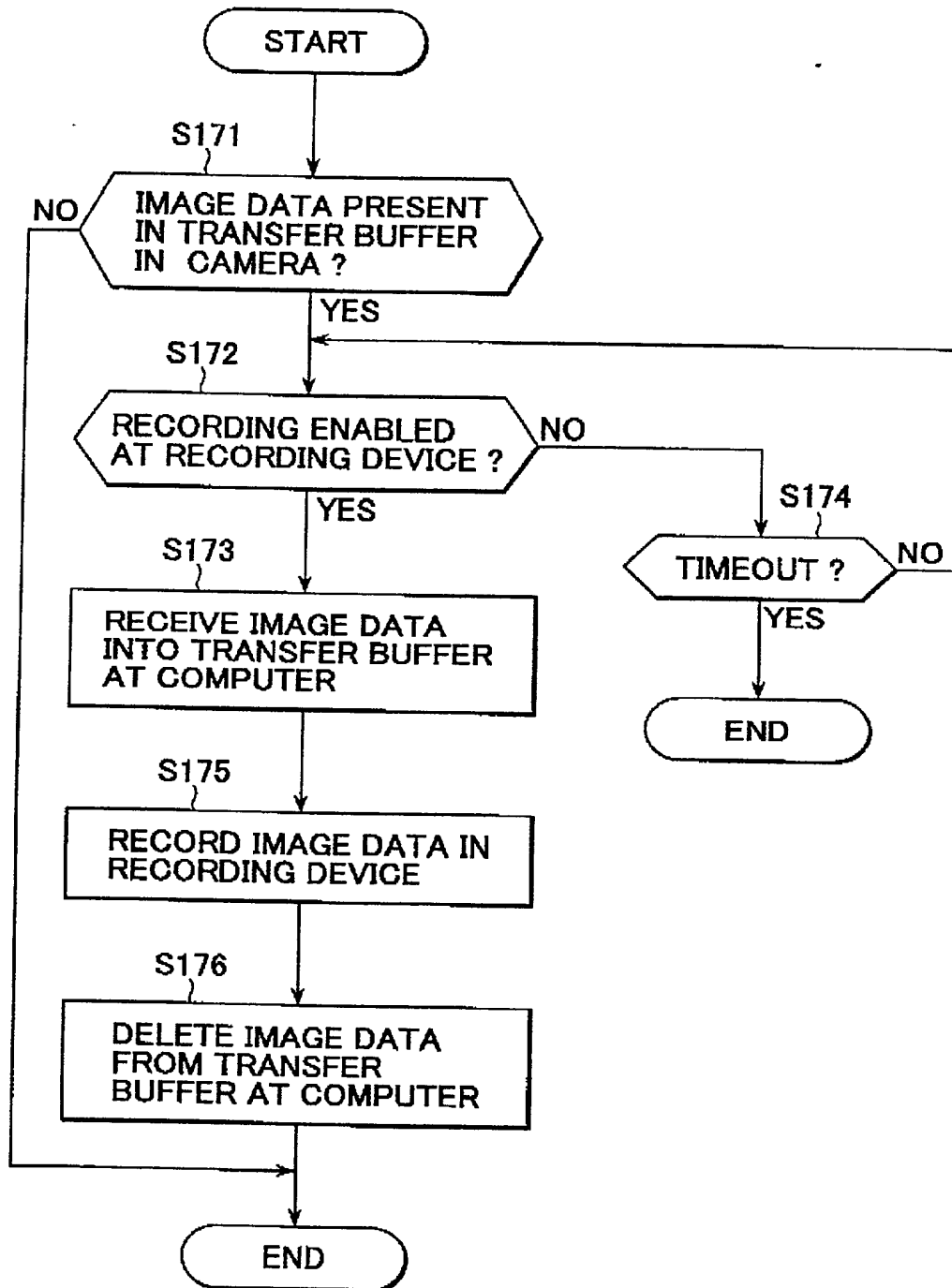


FIG.17

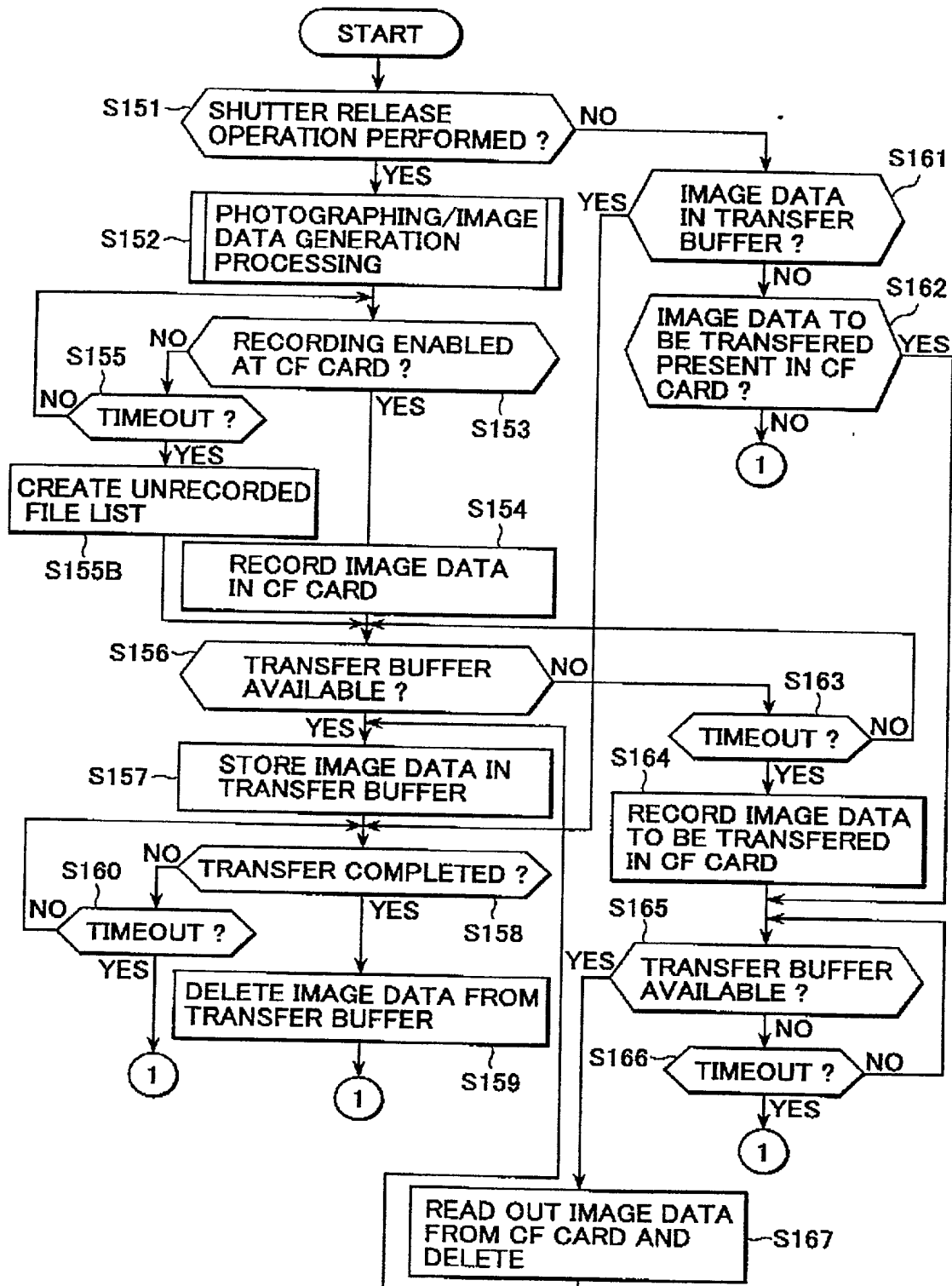


FIG.18

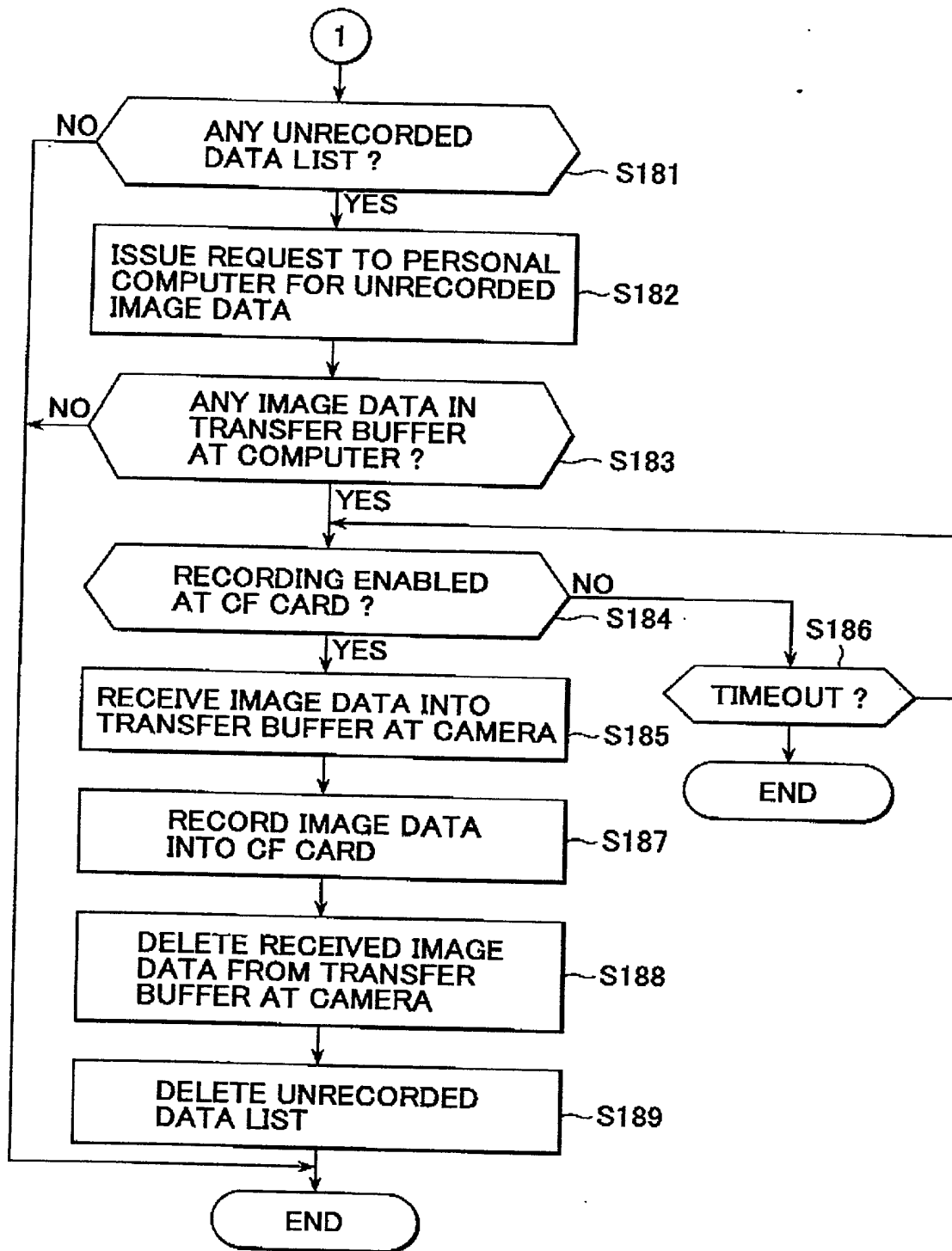


FIG.19

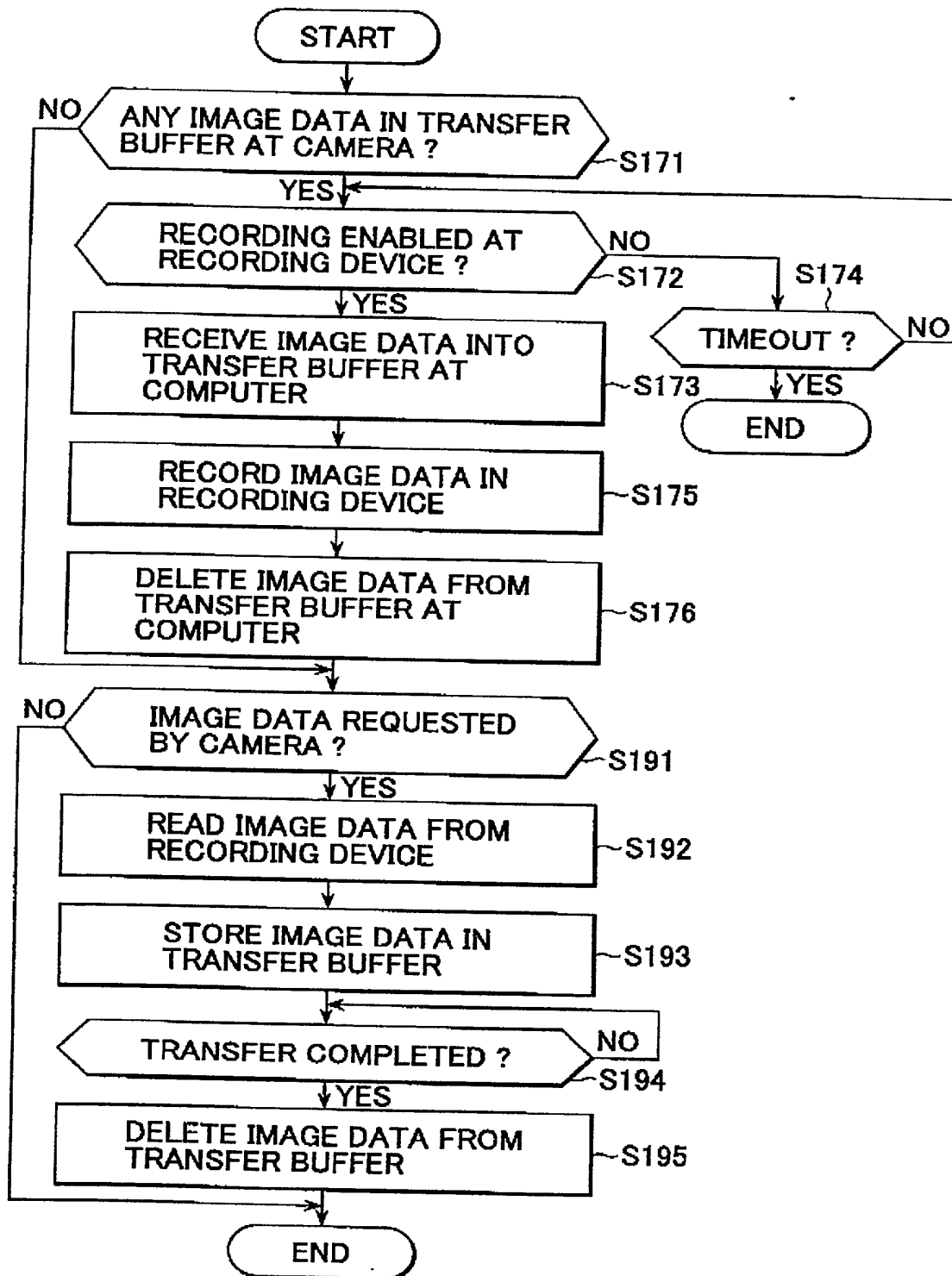
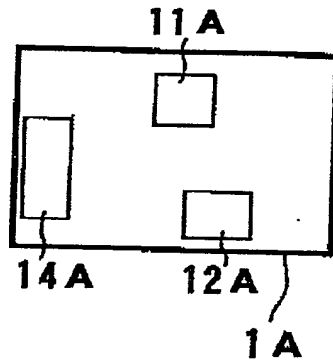
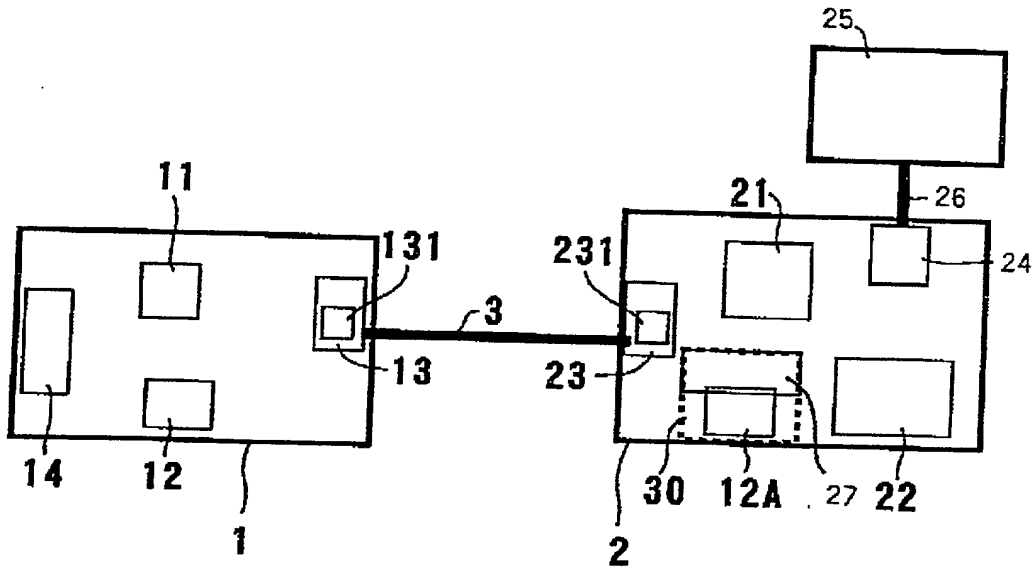


FIG.20



1, 1A	ELECTRONIC STILL CAMERA	2	PERSONAL COMPUTER
11, 11A	MPU	21	MPU
12, 12A	CF CARD	22	RECORDING DEVICE
13	INTERFACE CIRCUIT	23	INTERFACE CIRCUIT
14, 14A	IMAGE CAPTURING ELEMENT	30	SLOT
131	TRANSFER BUFFER MEMORY	231	TRANSFER BUFFER MEMORY

FIG.21

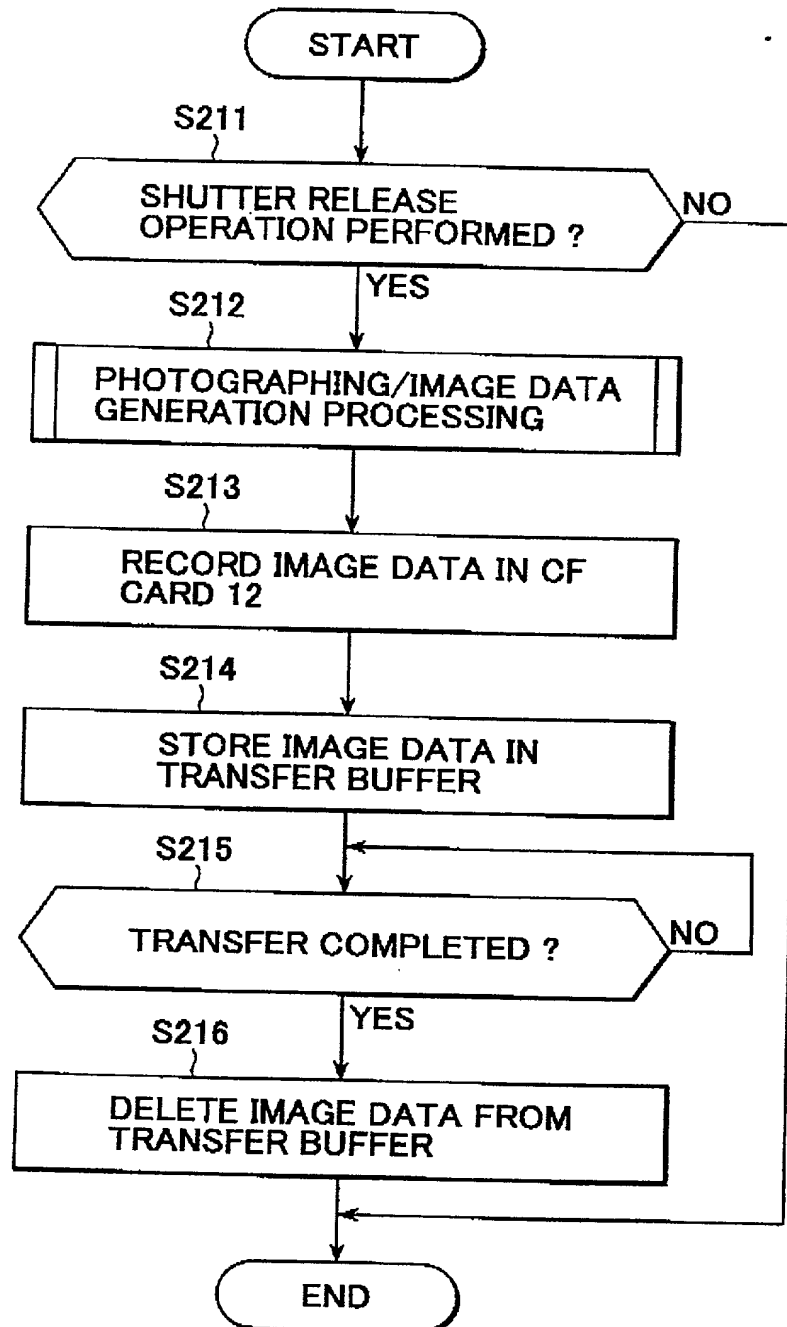


FIG.22

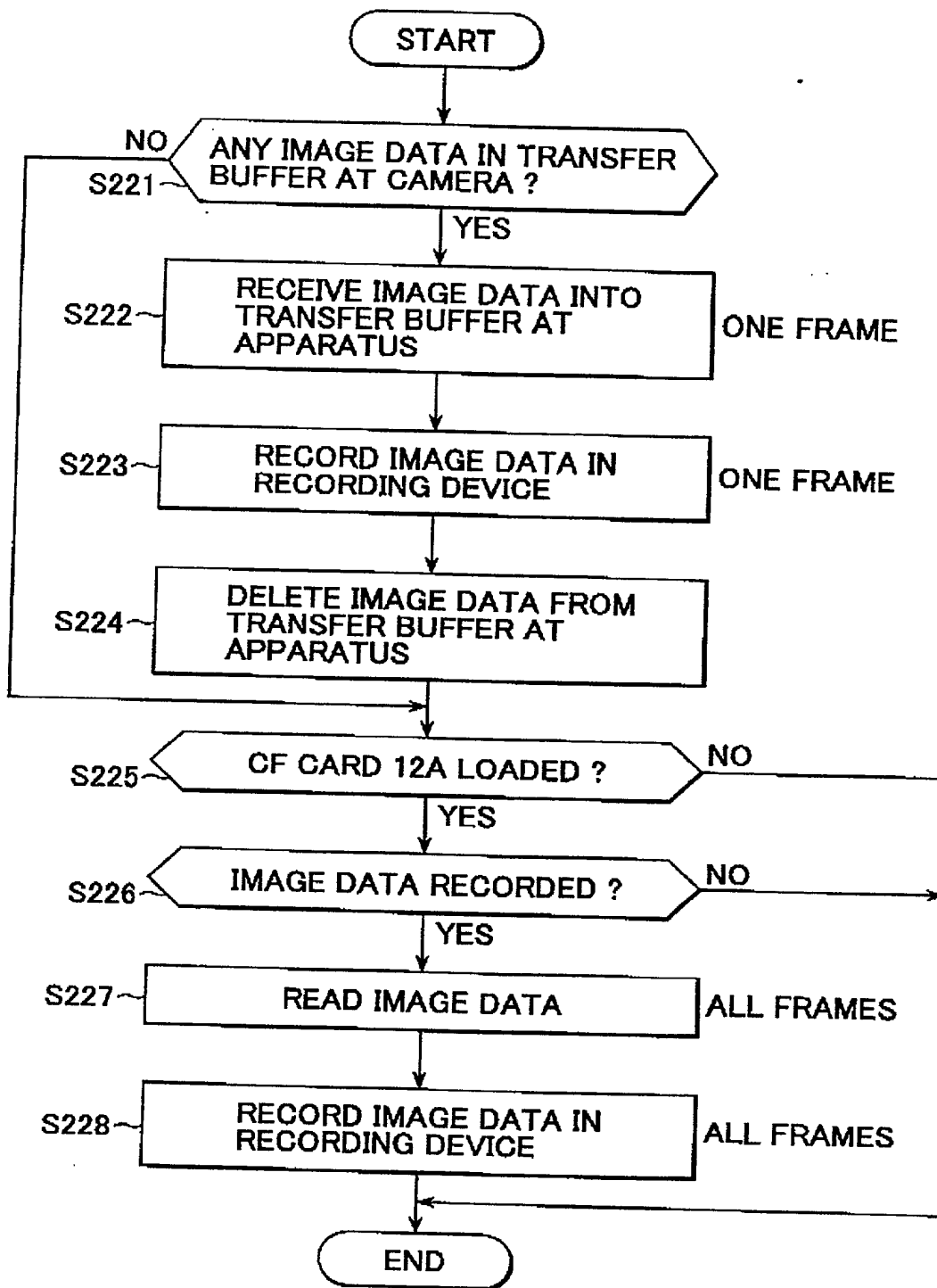


FIG.23

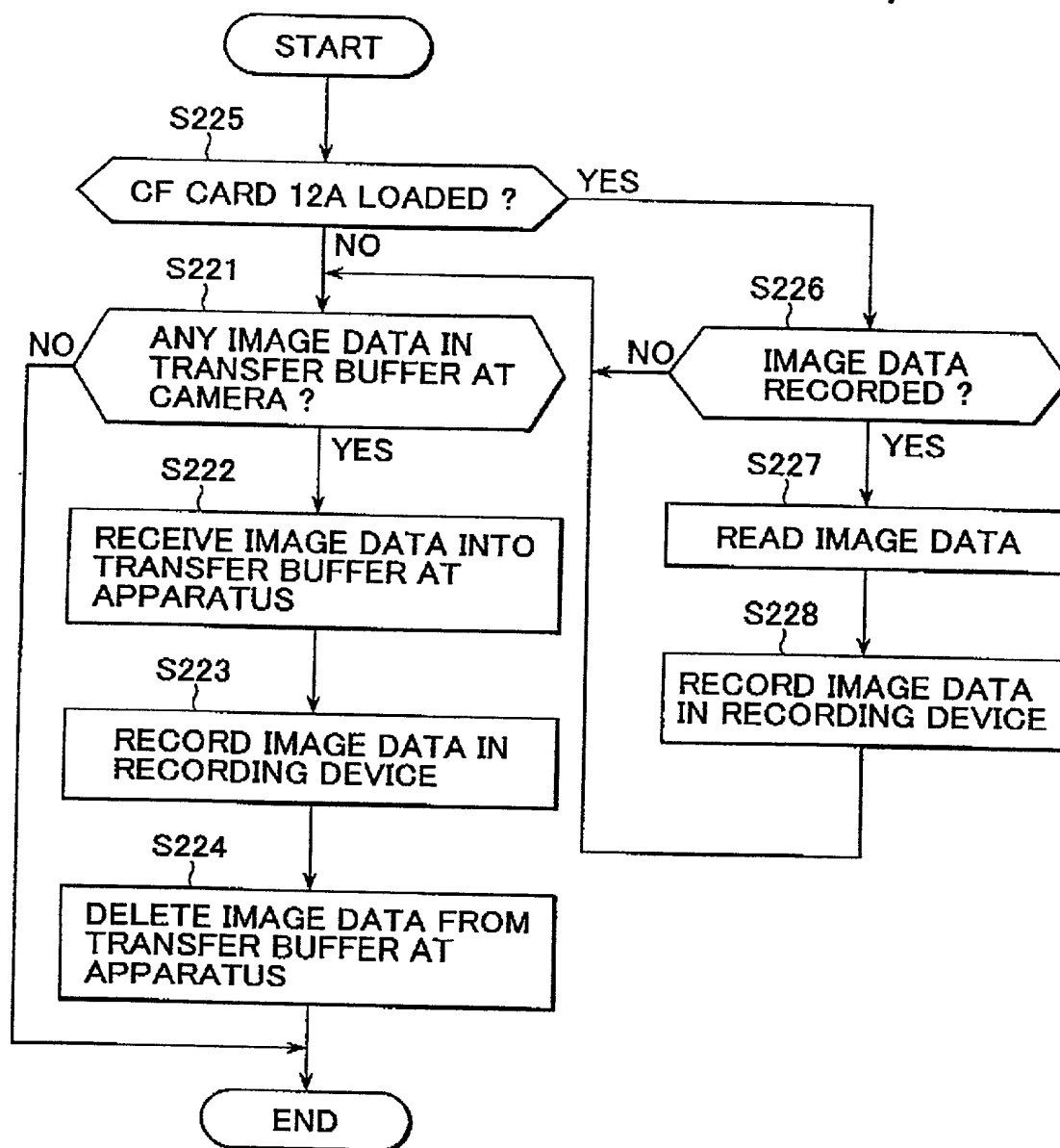


FIG.24A

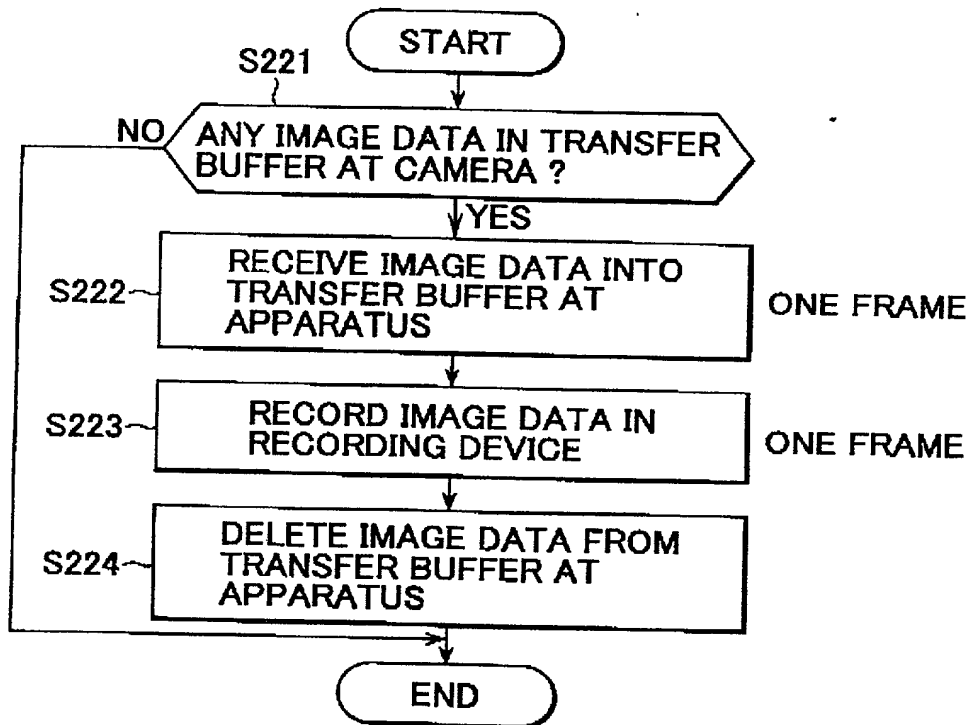


FIG.24B

